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## **CONFIRMATION NO. 5778**

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|--|--|--------------------|--------------|----|------------------------|-----|---------------------------------------|---------------|--|
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| ** CONTINUING DATA **********************************  |  |                    |              |    |                        |     |                                       |               |  |
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| 35 USC 119 (a-d) condit  | itions yes no Met a                    | ıfter              | SIAIEON      | 5  | SHEETS TO              |     | IAL                                   | INDEPENDENT   |  |
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| TITLE Technique to control tunneling currents in DRAM capacitors, cells, and devices   |  |                    |              |    |                        |     |                                       |               |  |
| ☐ All Fees   |  |                    |              |    |                        |     |                                       |               |  |
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| ☐ Other  |  |                    |              |    |                        |     |                                       |               |  |